

SYNCHRONIZING METHOD AND BRIDGE

Publication number: JP11261579

Publication date: 1999-09-24

Inventor: SUGITA TAKEHIRO; MAEJIMA YASUNARI

Applicant: SONY CORP

Classification:

- International: G06F13/38; G06F13/42; H04J3/06; H04L7/00; H04L12/46; H04L12/64; G06F13/38; G06F13/42; H04J3/06; H04L7/00; H04L12/46; H04L12/64; (IPC1-7): H04L12/28; G06F13/38; G06F13/42; H04L7/00

- European: H04J3/06C1; H04L12/64B

Application number: JP19980060932 19980312

Priority number(s): JP19980060932 19980312

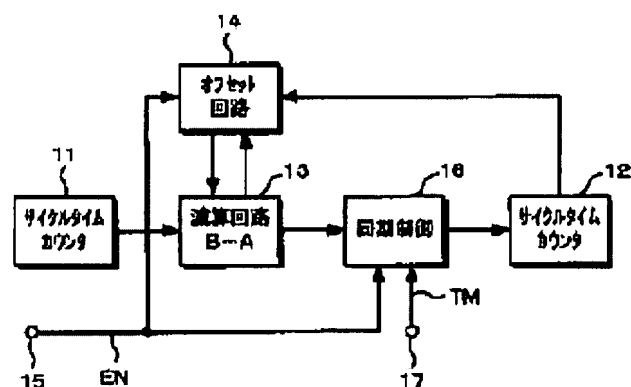
Also published as:

EP0942556 (A2)
US6879602 (B1)
EP0942556 (A3)
EP0942556 (B1)
DE69933724T (T2)

[Report a data error here](#)

Abstract of JP11261579

PROBLEM TO BE SOLVED: To establish synchronization without temporarily stopping communication when mutually connecting buses composed of plural nodes for transmitting/receiving data while maintaining mutual frame synchronism. **SOLUTION:** An offset value is found based on the difference of cycle time counters 11 and 12 of both the buses and stored in an offset circuit 14. When mutually connecting the buses, the value of the cycle time counter 12 is corrected just for the offset, compared with the value of the cycle time counter 11 and synchronized. Then, the internal time in data is exchanged with the processing time of a bridge for connecting the respective buses by correcting internal data in data transmitted through the bridge just for the offset of synchronizing timing between the respective buses.



Data supplied from the esp@cenet database - Worldwide